



PATENT  
8009-1014

IN THE U.S. PATENT AND TRADEMARK OFFICE

In re application of: Keisuke KANAMARU et al.  
Conf.: 4914  
Appl. No.: 10/618,704  
Group: 2825  
Filed: July 15, 2003  
Examiner: Naum Levin  
Title: DELAY OPTIMIZATION DESIGNING SYSTEM AND  
DELAY OPTIMIZATION DESIGNING METHOD FOR A  
LOGIC CIRCUIT AND CONTROL PROGRAM

**RESPONSE**

Assistant Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

August 11, 2005

Sir:

Responsive to the Office Action mailed April 12, 2005,  
it is respectfully requested that the present application be re-  
examined and reconsidered in the manner provided in 37 CFR  
§1.112, and in light of the remarks that follow.

**Remarks** begin on page 2 of this paper.